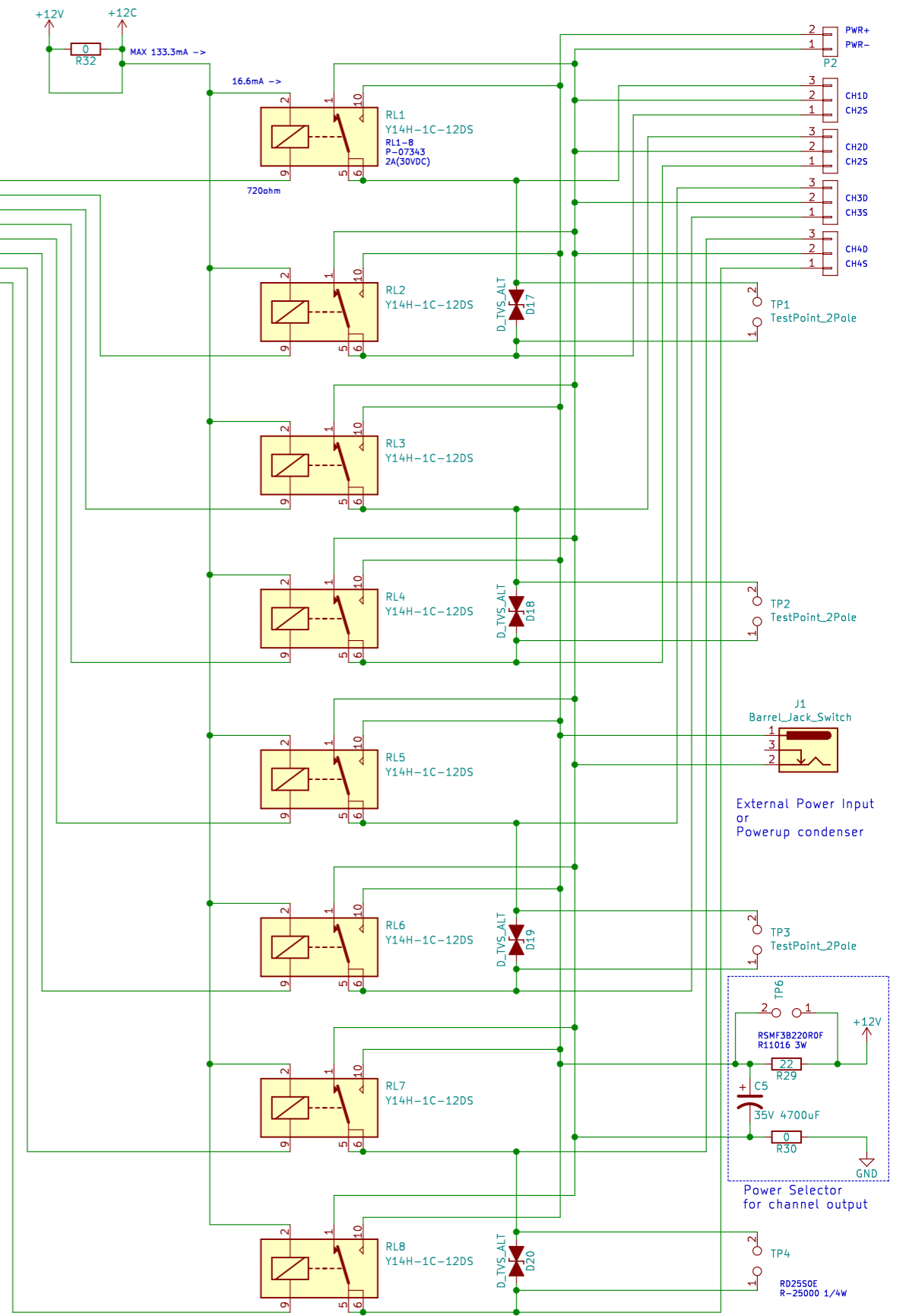
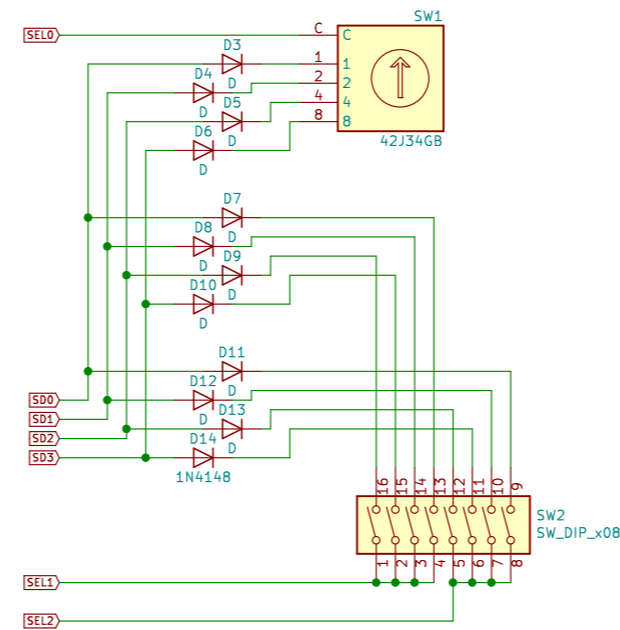
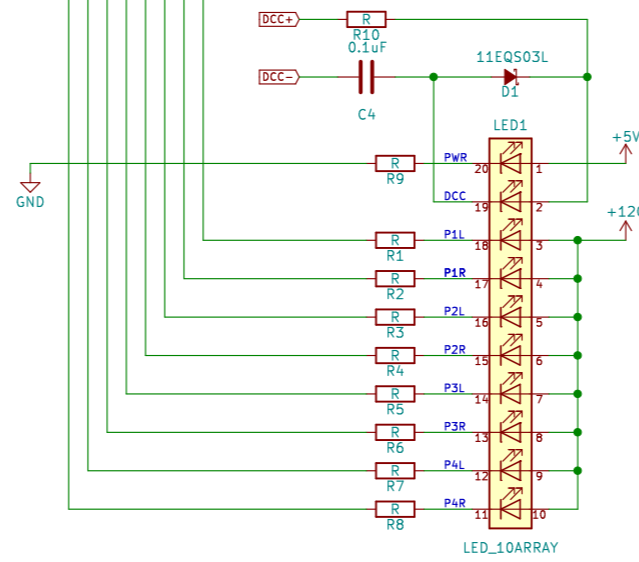
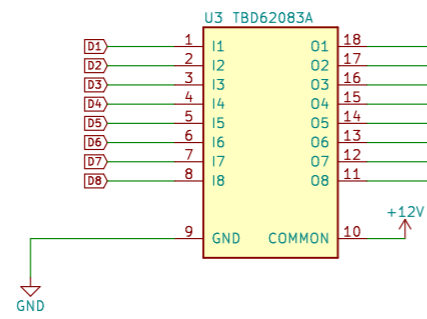
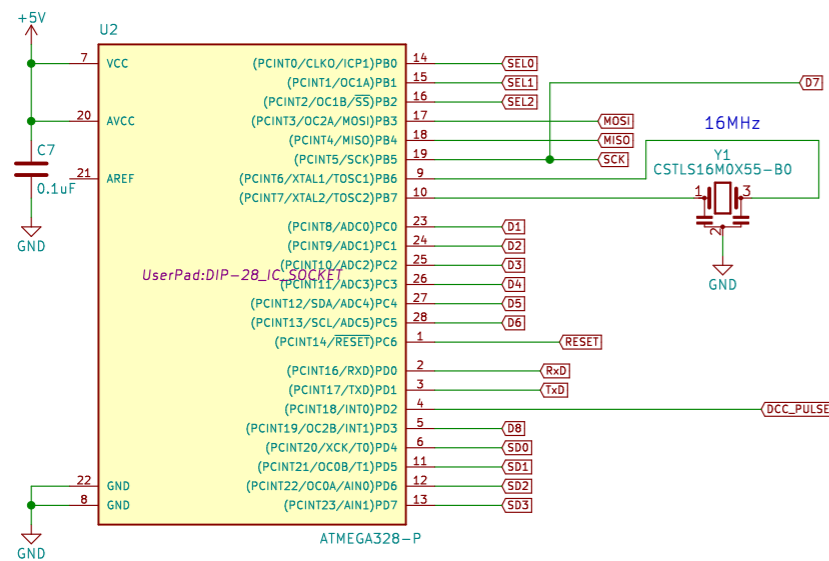
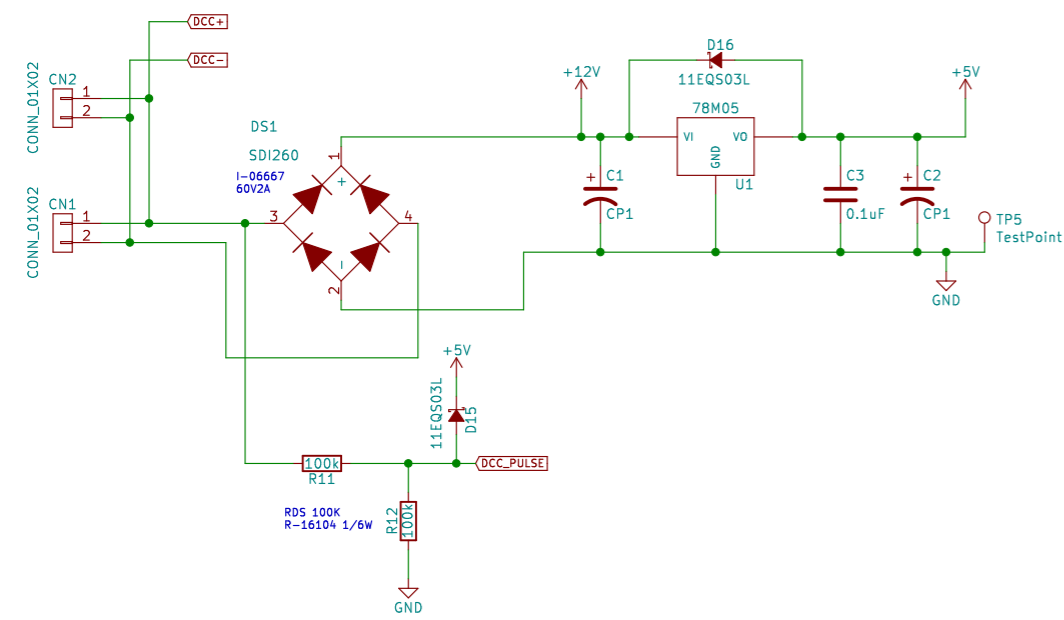
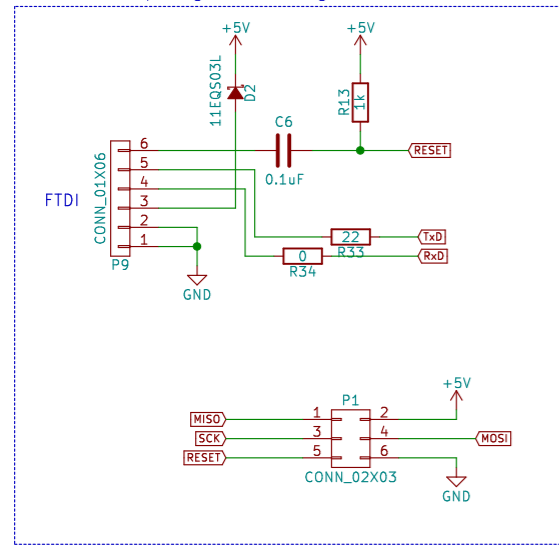


Optional circuit for CPU programming



Relay Decoder Traino- A100 rev2